

NOTES & ERRATA FOR PROJECTS PUBLISHED IN SILICON CHIP (2025)

Please note: errata apply primarily to the print edition of SILICON CHIP as online issues are normally changed when an error is identified. However some errata may still apply to the online edition; check carefully before making any changes to a project.

Power LCR Meter, March & April 2025: in Fig.8 on p36 of the March issue, the SI and SCK pins of IC5 are numbered incorrectly. SI is pin 6 and SCK is pin 5.

Pico/2/Computer, April 2025: boards with CH334F chips marked 13122E20 will not work unless resistors R54 and R55 are removed. Boards we sell with matching chips will come with those resistors removed so they function correctly. CH334F chips with batch code 1163FD43 are not affected and will work with the resistors in place. (05/25)

433MHz Transmitter, April 2025: Fig.3 on page 75 has the pin labels DATA and VCC transposed. The PCBs supplied are labelled correctly. (10/25)

Vintage Radio – Emerson 888, May 2025: there are two mistakes in the redrawn circuit diagram (Fig.1). R6 is shown connected to the wrong end of T2's secondary; it should connect to the lower side that goes to the base of TR2. Separately, the junction of C10 & R10 should connect to the base of TR3 (the bottom end of T3's secondary), rather than the top of T3's secondary. (07/25)

Boeing 737 MAX & MCAS, August 2025: the MCAS system controls the motors driving the trim tabs on the elevators, not the ailerons.

Thin-Film Pressure Sensor, August 2025: in Fig.3 on p35, the S pin should go to A0 and the + pin to 5V.

USB-C Power Monitor, part two, September 2025: the caption at the bottom of p79 states "A row of header pins can be fitted to CON5...". It should refer to CON3 instead. (09/26)

Digital Preamplifier part one, October 2025: in Fig.3 on page 34, the pins of IC7b and IC8b are swapped. The upper pin of each should be pin 5 (+) and the lower pin should be pin 6 (-). The connections of the other op amps in the circuit are correct. (12/25)

(2) in Fig.5 on page 36, pin 3 of the ADAU1467CORE BOARD connector should be labelled ADC_BCLK to match pin 73 of IC18. Also, in the pinout for Q1, the pins should be labelled (left-to-right) B, C & E and the tab is C. On p39, in Fig.8, pins 13 & 15 of IC15 go to IC17, not IC12. Finally, the designator CON8 for the 8×2-pin header that's the alternative to CON19 is not to be confused for the RCA output connectors, CON8.1-CON8.4. (02/26)

Serviceman's Log, October 2025: the photo at lower left on page 89 is of the wrong computer; it is an Acer, not a Toshiba P750. (12/25)

Four-colour e-paper display, Circuit Notebook, November 2025: diode D1 is shown backwards in the circuit diagram. Its cathode should be on the right. (01/26)

RP2350B Computer, November 2025: the pin numbering for CON8 in the circuit diagram, Fig.1, is mirrored compared to Table 1 and what's shown on the back panel. The function of each pair of pins (1 & 2, 3 & 4 etc) on CON8 in Fig.1 should be swapped to be consistent. The pin numbering in Fig.2 also needs to change. (01/26)

RGB LED Star Ornament, December 2025: in the circuit diagram, pin 2 of CON3 should connect to pin 11 of IC1 before the 330Ω resistor, rather than after. (02/26)

Power Electronics part 2, December 2025: in Fig.4 on page 32, the labels Zr and Zc are swapped in the high-pass filter circuit. (02/26)