

NOTES & ERRATA FOR PROJECTS PUBLISHED IN SILICON CHIP (2024)

Please note: errata apply primarily to the print edition of SILICON CHIP as online issues are normally changed when an error is identified. However some errata may still apply to the online edition; check carefully before making any changes to a project.

Microphone Preamplifier, February 2024: in Fig.8, the 150Ω resistor next to CON10 should be 330Ω and the two 3.9kΩ resistors above L2 should both be 3.0kΩ as described in the text on p33. (04/24)

Mains Power-Up Sequencer, February & March 2024: in the Fig.3 circuit diagram (February, p52), fuse F1 should be rated at 10A, not 1A. Also scope grabs 1 & 2 show the current drawn by three amplifiers in parallel, not one. (03/24)

Skill Tester 9000, April & May 2024: in Fig.2 on p64 of the April issue, IC6 at lower left (NE555) is incorrectly labelled IC2. In Fig.3 on p69, the V+ and RS labels for pins 4 & 8 of IC9 and IC14 are swapped (pin 4 is RS, pin 8 is V+ in each case). In the text on p70, the 33nF, 470nF & 1uF values mentioned in the third paragraph of the right-hand column should be 10uF and 22uF instead. Finally, on p71, the 470nF value mentioned in the fourth line of the left-hand column should instead read 33nF. (06/24)